
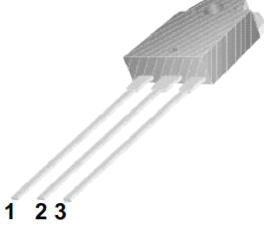
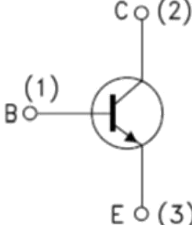



<div style="text-align: center;">  <p>奥德利[®] AUDLEY</p> <h2 style="margin: 0;">2SD718</h2> </div> <p>Features:</p> <ul style="list-style-type: none"> <input type="checkbox"/> High Current Capability: $I_C=8A$ <input type="checkbox"/> High Power Dissipation <input type="checkbox"/> Extended Safe Operating Area. <input type="checkbox"/> NPN Transistor <input type="checkbox"/> Complement to 2SB688 <input type="checkbox"/> 100% Avalanche Tested 	<div style="text-align: center;"> <p>TO-3P</p>  <p>1 2 3</p>  </div> <div style="text-align: right; margin-top: 10px;"> <p>1. Base (B)</p> <p>2. Collector (C)</p> <p>3. Emitter (E)</p> </div> <div style="text-align: right; margin-top: 10px;">  </div>
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Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	120	V
V_{CEO}	Collector-emitter voltage	Open base	120	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		8	A
I_B	Base current		0.8	A
P_T	Total power dissipation	$T_C=25^{\circ}C$	80	W
T_j	Junction temperature		150	°C
T_{stg}	Storage temperature		-55~150	°C

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-50mA, I _B =0	120			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-5A; I _B =-0.5A			2.5	V
V _{BE}	Base-emitter voltage	I _C =-5A; V _{CE} =-5V			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-120V; I _E =0			10	μA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			10	μA
h _{FE}	DC current gain	I _C =-1A; V _{CE} =-5V	55		160	
f _T	Transition frequency	I _C =-1A; V _{CE} =-5V		10		MHz
C _{ob}	Output capacitance	I _E =0; V _{CB} =-10V; f=1MHz		280		pF

◆ h_{FE} Classification

R	O
55-110	80-160

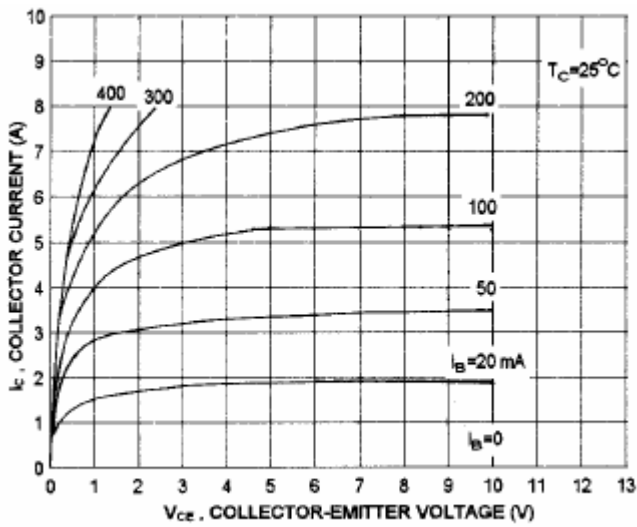


Fig.3 Static Characteristic

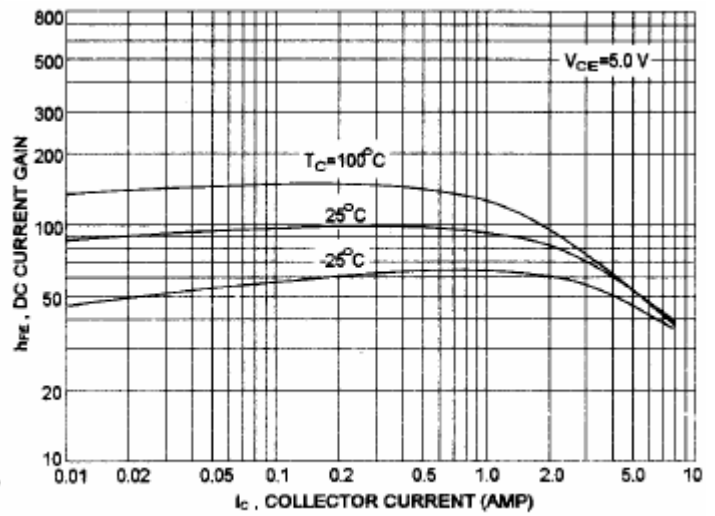


Fig.4 DC current Gain

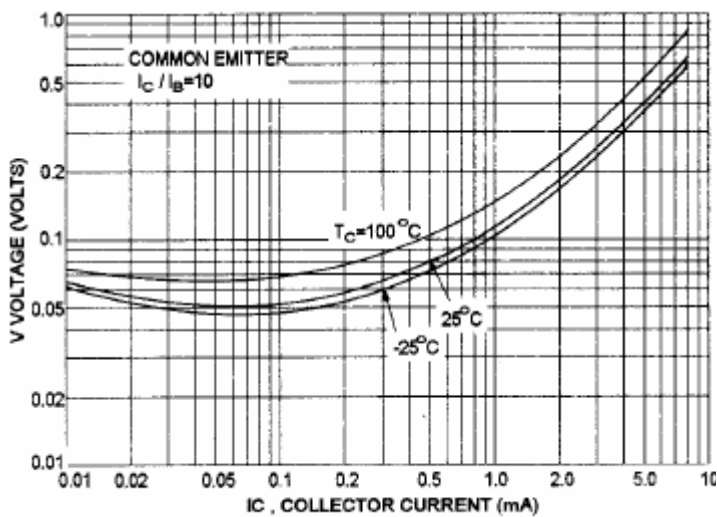


Fig.5 Collector-Emitter Saturation Voltage

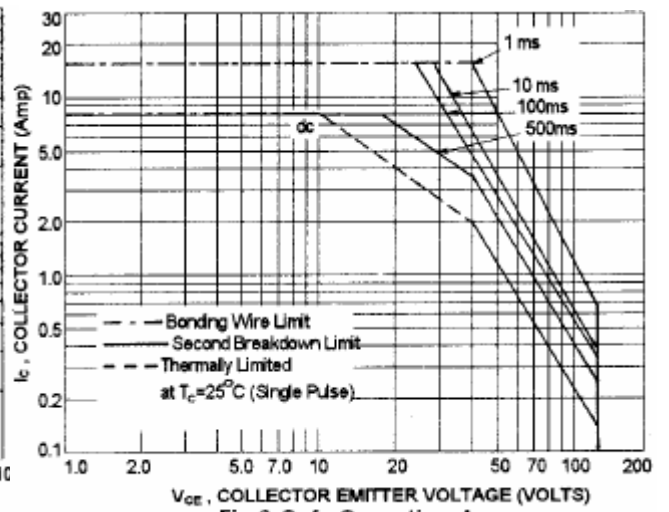


Fig.6 Safe Operating Area

